

KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE: VARANGAL - 15

(An Autonomous Institute under Kakatiya University)

DEPARTMENT OF CIVIL ENGINEERING

M.tech II-Semester Class Work Time Table 2019-20

M.TECH

Class in charge: Sri. N.SRIKANTH

Room No: BIV 003

W.E.F : 20-JAN-2020

Day/ Time	09.40 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 01.00	01.00 - 02.00	02.00 - 02.50	02.50 - 03.40	03.40 - 04.30
Monday	FEM	TOE	LIBRARY	CEA	L U N C H	DCB	CTE	CCM
Tuesday	EQRDS	-----SE LAB-----				CTE	DCB	-
Wednesday	TOE	CCM	CTE	DCB		EQRDS	-	-
Thursday	EQRDS	CTE	CCM	FEM		TOE	FEM	DCB
Friday	CCM	FEM	TOE	EQRDS		-----DP LAB-----		
Saturday								

S.No	COURSE CODE- COURSE NAME	FACULTY
1	Theory Of Elasticity(TOE)	Ms. K.Manasa (KMA)
2	Finite Element Method(FEM)	Sri.K.Srujan Varma (KSV)
3	Design Of Concrete Bridges(DCB)	Sri.D.Hari Krishna (DHK)
4	Construction Techniques & Equipments(CTE)	Sri.A.Bhaskar (AB)
5	Earthquake Resistant Design of RCC Structures (EQRDS)	Sri. N.Srikanth (NSK)
6	Construction And Contract Management (CCM)	Sri.A.Suchith Reddy (ASR)
7	Design Project Laboratory (DP LAB)	Sri. S.RajaShekar (SRS)
8	Structural Engineering Lab (SE LAB)	Ms. K.Manasa(KMA)

By
In-charge Time Tables
A. BHASKAR/P.T. ABHILASH

By
Co-Ordinator Time Tables
Dr.M. ANDAL

By
Head, Dept. of Civil Engg.
Dr. M.VEERA REDDY

By
for Dean Academic Affairs
Dr. K.SIVANI

By
Principal.
Dr K.ASHOKA REDDY



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE: WARANGAL-15
(An Autonomous Institute under Kakatiya University, Warangal)
DEPARTMENT OF MECHANICAL ENGINEERING
M.TECH. (DESIGN ENGINEERING) II - SEMESTER
Class Work Time-Table for the Academic Year 2019 - 20

Class Teacher: Dr. G. Sai Kumar

Class Room: SH 3-2-4

W.E.F: 20.01.2020

Day/Time	9.40 10.30	10.30 11.20	11.20 12.10	12.10 1.00	1.00 2.00	2.00 2.50	2.50 3.40	3.40 4.30	
Monday	A & R	AFEA	CM	FDOM	L U N C H	ADMC	AMS	RC	
Tuesday	CM	AMS	FDOM	RC		AFEA	ADMC	ADMC	
Wednesday	AMS	A & R	A & R	CM		B.TECH CLASSWORK			
Thursday	FDOM	FEM LAB				CM	A & R	ADMC	
Friday	AMS	FDOM	AFEA	AFEA		AUTOMATION & ROBOTICS LAB			
Saturday	T & P ACTIVITY								

- Advanced Finite Element Analysis (AFEA) : Ms. V. Laxmi Priayanka
 Composite Materials (CM) : Dr. G. Sai Kumar
 Advanced Design of Machine Components (ADMC) : Sri. V. Pradeep
 Automation & Robotics (A&R) : Sri. V. Prasanna
 ELECTIVE - III - Fault Diagnosis of Machines : Sri. V. Rajesh
 ELECTIVE - IV - Advanced Material Science : Sri. P. Sreedhar
 FEM Lab : Sri. A. Harikumar & Ms. A. Keerthi
 Automation & Robotics Lab : Sri. V. Prasanna & Sri. D. Sammaiah

ALMANAC	
COMMENCEMENT OF CLASSWORK	20.01.2020
Class work duration (Phase - I)	20.01.2020 to 29.02.2020
Mid Semester Examination - I (MSE-I)	02.03.2020 to 07.03.2020
Class work duration (Phase - II)	09.03.2020 to 25.04.2020
LAST DAY OF INSTRUCTION	25.04.2019
Mid Semester Examination - II (MSE-II)	27.04.2020 to 02.05.2020
Commencement of End Semester Examination (ESE) (Theory & Practicals)	04.05.2020
COMMENCEMENT OF CLASSWORK (Commencement of Industrial Training for Next Academic Year)	01.06.2020

I/C Time Tables
 Sri. A. HARI KUMAR
 Sri. MOHD. SAMEER

I/C Overall Time Tables
 Dr. M. ANDAL

Head, MED
 Dr. K. SRIDHAR

Dean, Academics
 Dr. K. SIVANI

Principal
 Prof. K. ASHOKA REDDY

KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE WARANGAL
I M.Tech. (II-SEMESTER) CLASS WORK TIME TABLE 2019-2020
ELECTRONICS & COMMUNICATION ENGINEERING
DIGITAL COMMUNICATIONS

M.Tech (DC)


Class In-Charge: E.Suresh

Room No. BIV-001A

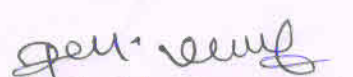
W.e.f. 20.01.2020

Day / Time	9.40 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00		2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Monday	DIP	ASP	SC	CT	L	←----DC Lab ----→ BK/GKK		
Tuesday	SC	RSP	ASP	-		U	CT	CSM
Wednesday	CSM	DIP	RSP	SC	N	←----CSM Lab ----→ KS/DSK		
Thursday	CT	RSP	ASP	ECEA		C	CSM	DIP
Friday	ASP	RSP	SC	CSM	H	DIP	CT	-
Saturday	-	-	-	-			--	--

- | | |
|--|---|
| CSM : Communication System Modeling (P14DC201) | : Dr.B.Rama Devi (BRD) |
| CT : Coding Theory (P14DC202) | : E.Suresh (ES) |
| ASP : Adaptive Signal Processing (P14DC203) | : A.Vijaya (AV) |
| RSP : Radar Signal Processing (P14DC204) | : Dr.G.Raghotham Reddy (GRR) |
| SC : Satellite Communications (P14DC205B) | : S.P.Girija (SPG) |
| DIP : Digital Image Processing (P14DC206C) | : P.Yugander (PY) |
| CSM Lab : Communication System Modeling Laboratory (P14DC207) | : Dr.K.Sowjanya (KS) / D.Santhosh Kumar (DSK) |
| DC Lab : Digital Communication Laboratory (P14DC208) | : B.Komuraiah (BK)/ G.Kranthi Kumar (GKK) |
| T&P /TASK : Training & Placement /Telangana Assoc. for Skill & Knowledge | : A.Srinvas / K.Ramudu/V.Raju |
| ECEA : ECE Association Activity | : B.Komuraiah/Syed Zaheeruddin /Dr.K.Sowjanya |


I/c Time Table


Time Table Coordinator


Head of the Department


Dean, Academic Affairs


Principal

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE: WARANGAL
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
CLASS WORK TIME TABLE FOR THE ACADEMIC YEAR 2019-2020
M.Tech II SEMESTER

Course Coordinator: S. Naga Raju

Room No: B IV- 014

W.e.f: 20.01.2020

Day/Time	9:40 to 10:30	10:30 to 11:20	11:20 to 12:10	12:10 to 1:00	1:00 to 2:00	2.00 to 2.50	2.50 to 3.40	3.40 to 4.30
Monday	SADP	BDA	ADM	SQAT	L U N C H	ML	CC	SADP
Tuesday	CC	ST Lab SD Lab +AP+PR				ADM	BDA	SQAT
Wednesday	CC	ML	SQAT	BDA		SADP	ADM	Remedial Class
Thursday	SADP	DE Lab RDE Lab +NG+PR				ML	SQAT	CC
Friday	ADM	BDA	ML	Remedial Class				
Saturday								

SADP
SQAT
ADM
CC

Software Architecture and Design Patterns
Software Quality Assurance and Testing
Advanced Data Mining
Cloud Computing

A. Praveen (AP)
Dr. P. Niranjan (PNR)
Dr. N. Gayathri (NG)
B. Sridhara Murthy (BSM)

ML (Elective-III)
BDA(Elective-IV)

Machine Learning
Big Data Analytics

S. Naga Raju(SN)
S. Swapna (SS)

ST Lab
DE Lab

Software Testing Lab
Data Engineering Lab

A. Praveen+P.Rajitha
Dr. N. Gayathri+ P.Rajitha


I/c. Time-Tables


Co-Ordinator
Time-Tables


Head of the Department


Dean, Academic Affairs


Principal

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL - 15

(An Autonomous Institute under Kakatiya University, Warangal)

VLSI & Embedded Systems

M.Tech II Semester Class work Time Table (2019-2020)


Class Teacher: Sri O.Anjaneyulu

Room No. BIV 001A

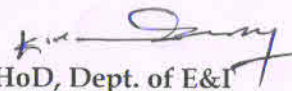
w.e.f. 20.01.2020

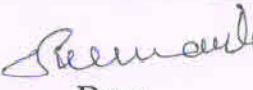
Day	9.40 - 10.30	10.30 -11.20	11.20 -12.10	12.10 - 1.00	L U N C H	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Mon	ASICD	HSCD	LPVLSI	RTOS		←-----ESD LAB-----→ KSJ		
Tue	RTOS	LPVLSI	HSCD	ASICD		MSD	IOT	←--LAB DUTY--→
Wed	IOT	←-----MSD LAB-----→ OA				ASICD	MSD	LPVLSI
Thu	HSCD	RTOS	IOT	MSD		←-----LAB DUTY-----→		
Fri	RTOS	MSD	LPVLSI	IOT		ASICD	HSCD	←--LAB DUTY--→
Sat	CCA/T&P							

MSD	Mixed Signal Design	:	Sri O.Anjaneyulu (OA)
RTOS	RTOS for Embedded Systems	:	Smt K.Shailaja (KSJ)
LPVLSI	Low Power VLSI Design	:	Sri B.jeevan (BJ)
HSCD	Hardware & Software Co-Design	:	Sri B.Venu Maheswar (BVM)
ASICD(Elective-III)	ASIC Design	:	Sri G.Raju (GR)
IOT(Elective-IV)	Internet of Things	:	Dr.K.Srinivas (Dr.KSC)
ESD Lab	Embedded System Design Laboratory	:	Smt K.Shailaja (KSJ)
MSD Lab	Mixed Signal Design Laboratory	:	Sri O.Anjaneyulu (OA)


I/c Time Table
(Smt B.Smitha)


Coordinator
(Time Tables)
Dr.M.Andal


HoD, Dept. of E&I
(Dr.K.Venu Madhav)


Dean,
Academic Affairs
(Dr.K.Sivani)


PRINCIPAL
(Dr.K.Ashoka Reddy)

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL-15
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
 Classwork Timetable for the year 2019-2020
II SEMESTER of M.Tech. (POWER ELECTRONICS)

2 PE

Class Teacher: Dr. A. MADHUKAR RAO

Classroom: SH 3-2

w.e.f. 20.01.2020

DAY\TIME	9.40-10.30	10.30-11.20	11.20-12.10	12.10-13.00	LUNCH	14.00-14.50	14.50-15.40	15.40-16.30
MONDAY	MMA	AIAEE	FACTS	PECACD		PQ	APE	LIBRARY
TUESDAY	ED LAB			FACTS		MMA	AIAEE	PQ
WEDNESDAY	PECACD	APE	PQ	EEEE AUDITORIUM		MMA	FACTS	AIAEE
THURSDAY	AIAEE	APE	PECACD	PQ		ASEE LAB		
FRIDAY	FACTS	APE	MMA	PECACD				
SATURDAY								

- PECACD : Power Electronic Control of AC drives
 MMA : Machine Modeling & Analysis
 APE : Advanced Power Electronics
 AIAEE : Artificial Intelligence Applications in Electrical Engineering
 PQ : Power Quality
 FACTS : Flexible AC Transmission System
 ED LAB : Electrical Drives Laboratory
 ASEE LAB : Alternative Sources of Electrical Energy Laboratory
 EEEA : Electrical & Electronics Engineering Association

- Sri P. Mahesh
 Smt. M. Spandana
 Sri C. Pavan Kumar
 Dr. A. Madhukar Rao
 Sri K. Srinivas
 Sri V. Srinivas
 Dr. A. Madhukar Rao, Sri P. Mahesh
 Sri K. Srinivas, Smt. M. Spandana

P. Nagun Reddy
 I/c. Time Table

[Signature]
 Coordinator, Time Tables

C. Venkatesh
 Head of the Department

[Signature]
 Dean, Academic Affairs

[Signature]
 Principal